



**END SEMESTER EXAMINATION TIMETABLE, JAN, 2025**  
**BACHELOR OF TECHNOLOGY (B. Tech.) All Branches**  
**FIRST YEAR, SEMESTER – I (For 2024-25 Batch Under Autonomy)**

Date: 06<sup>h</sup> Jan, 2025

**Course Name: Engineering Graphics**

Day & Date	DIV / Batch	Roll No.		Time	Venue
		From	To		
10-01-2025	C2 (All Batches)	C064	C126	09.00 AM to 11.00 AM	Advance Computer Lab (2nd floor)
	C3 (All Batches)	C127	C189	12.00 PM to 02.00 PM	
11-01-2025	C1 (All Batches)	C001	C063	09.00 AM to 11.00 AM	
	I1 (All Batches)	I001	I062	12.00 PM to 02.00 PM	
13-01-2025	I2 (All Batches)	I063	I125	8.30 AM to 10.30 AM	
	D2 (All Batches)	D064	D126	11.30 AM to 01.30 PM	
	D3 (All Batches)	D127	D190	2.30 PM to 04.30 PM	
14-01-2025	D1 (All Batches)	D001	D063	09.00 AM to 11.00 AM	
	I3 (All Batches)	I126	I187	12.00 PM to 02.00 PM	

All the students are hereby informed to report at least **15 minutes** prior to the scheduled time in **respective laboratories**.

**Important Note:** Changes if any, in the timetable, shall be communicated on the College website ([djsce.ac.in](http://djsce.ac.in)).

Ms. Alka Shukla  
Controller of Examinations

Dr. Hari Vasudevan  
Principal